

Notice of References Cited	Application/Control No. 10/646,658	Applicant(s)/Patent Under Reexamination LIANG, CHUNLIN	
	Examiner Eric B. Chen	Art Unit 1765	Page 1 of 1

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NON-PATENT DOCUMENTS

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	V	Wolf et al., Silicon Processing for the VLSI Era, 1986, Lattice Press, Vol. 1, pp. 280-281.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.